# **Designated client product**

This product will be discontinued its production in the near term. And it is provided for customers currently in use only, with a time limit. It can not be available for your new project. Please select other new or existing products.

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New Japan Radio Co.,Ltd.

## www.njr.com

### **DUAL OPERATIONAL AMPLIFIER**

#### GENERAL DESCRIPTION

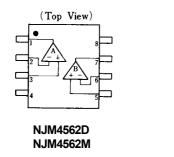
JRC

The NJM4562 integrated circuit is а high-gain, wide-bandwidth, low noise, dual operational amplifier capable of driving 20V peak-to-peak into  $600\Omega$  loads. The NJM4562 is frequency compensated for closed loop gains greater than 10. The NJM4562 combines many of the features of the popular NJM4558 as well as providing the capability of wider bandwidth, and higher slew rate and less noise make the NJM4562 ideal for audio preamplifiers, active filters, telecommunications, and many instrumentation applications. The availability of the NJM4562 in the surface mounted micro package allows the NJM4562 to be used in critical applications requiring very high packing densities.

#### ■ FEATURES

- Operating Voltage
- Low Input Noise Voltage
- Package Outline
- Bipolar Technology

#### ■ PIN CONFIGURATION

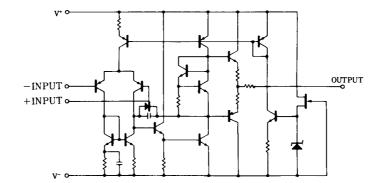


(±4V~±18V)

DIP8, DMP8

(0.6µVrms typ.)

#### ■ EQUIVALENT CIRCUIT (1/2 Shown)



PIN FUNCTION 1.A OUTPUT 2.A –INPUT 3.A +INPUT 4.V 5.B +INPUT 6.B –INPUT 7.B OUTPUT

8.V<sup>+</sup>

#### PACKAGE OUTLINE



NJM4562D

NJM4562M

#### ■ ABSOLUTE MAXIMUM RATINGS

			( Ta=25°C )
PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V <sup>+</sup> /V <sup>-</sup>	± 18	V
Differential Input Voltage	VID	± 30	V
Input Voltage	VIC	±15 (note)	V
Power Dissipation	P <sub>D</sub>	( DIP8 ) 500 ( DMP8 ) 300	mW
Operating Temperature Range	T <sub>opr</sub>	-40~+85	°C
Storage Temperature Range	T <sub>stg</sub>	-40~+125	D°

(note) For supply voltage less than ±15V, the absolute maximum input voltage is equal to the supply voltage.

#### ■ ELECTRICAL CHARACTERISTICS

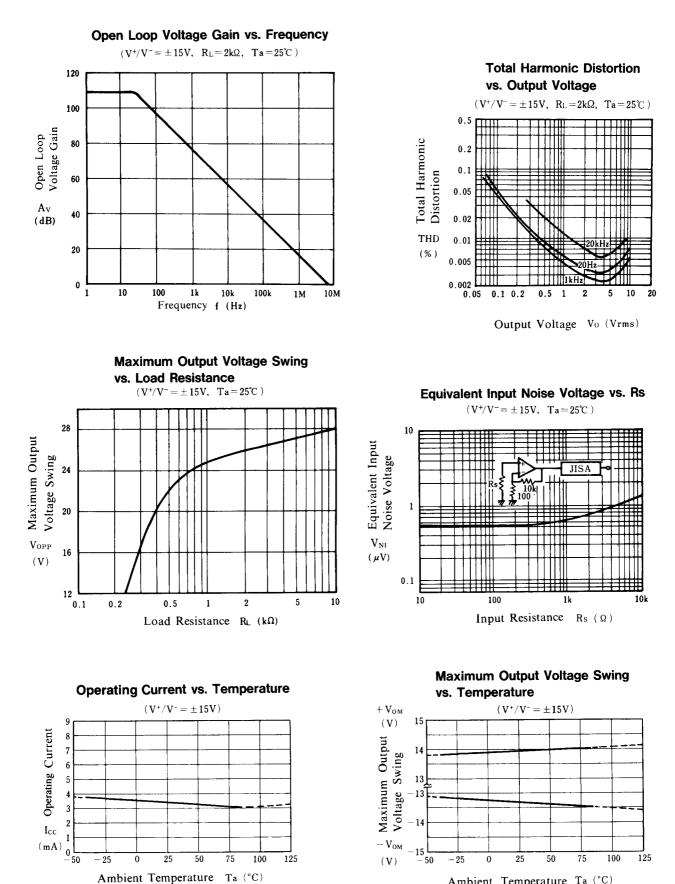
				(Ta=25°C,V*/V*=±15V)		
PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Input Offset Voltage	V <sub>IO</sub>	R <sub>s</sub> ≤10kΩ	-	0.5	6	mV
Input Offset Current	l <sub>iO</sub>		-	5	200	nA
Input Bias Current	IB		-	100	500	nA
Input Resistance	R <sub>IN</sub>		0.3	5	-	MΩ
Large Signal Voltage Gain	Av	$R_L \ge 2k\Omega, V_O = \pm 10V$	86	110	-	dB
Maximum Output Voltage Swing 1	V <sub>OM1</sub>	R <sub>L</sub> ≥10kΩ	± 12	± 14	-	V
Maximum Output Voltage Swing 2	V <sub>OM2</sub>	R <sub>L</sub> ≥2kΩ	± 10	± 13	-	V
Input Common Mode Voltage Range	VICM		± 12	± 14	-	V
Common Mode Rejection Ratio	CMR	R <sub>S</sub> ≤10kΩ	70	90	-	dB
Supply Voltage Rejection Ratio	SVR	R <sub>s</sub> ≤10kΩ	76.5	90	-	dB
Operating Current	Icc		-	3.5	5.7	mA
Equivalent Input Noise Voltage	V <sub>NI</sub>	R <sub>S</sub> =300Ω,JISA	-	0.6	-	μVrms

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#### ■ TYPICAL CHARACTERISTICS



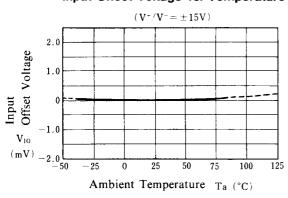
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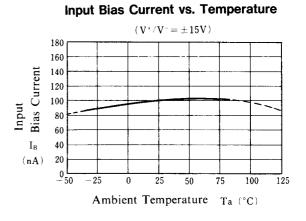
- 3 -

Ambient Temperature Ta (°C)

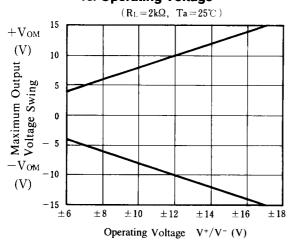
#### TYPICAL CHARACTERISTICS

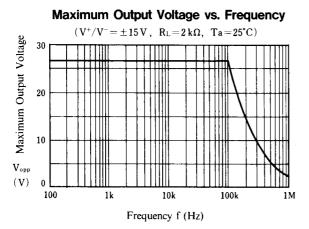


# Input Offset Voltage vs. Temperature

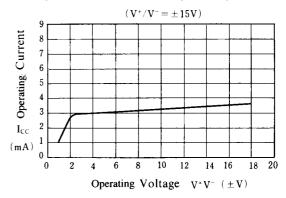


Maximum Output Voltage Swing vs. Operating Voltage





#### **Operating Current vs. Operating Voltage**



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